FORM PTO-1449
INFORMATION DISCLOSURE STATEMENT OF

	Page 1 of 1
ATTY. DOCKET NO.	APPLICATION NO.
1875.4360005	10/697,289
INVENTORS	
CATALASAN et al.	
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le	AS	1	Catalasan et al., U.S. Patent Appl. No. 10/697,889, filed October 31, 2003, entitled "Method For Manufacturing A Memory Cell For Modification Of Revision Identifier In An Integrated Circuit Chip," 53 pages.									
Cu	AT	1	Catalasan <i>et al.</i> , Cell For Modifica	, U.S. Pation O	atent f Defa	Appl. ult Re	No. 10/697,280 egister Values I	6, filed Oc n An Integ	stober 3	I, 2003, entit ircuit Chip,"	lled "Memory 59 pages.	
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